ABSTRACT

In a data processing circuit which sequentially performs a plurality of data processings, data processors assign, to data, processing destination identifiers indicating subsequent processing destinations determined based on information included in the data, and a switch provides the data to the subsequent processing destinations based on the processing destination identifiers. Alternatively, a processing destination identifier assigning portion provides, to the switch, data in which the processing destination identifiers indicating all data processing procedures determined based on the information included in the data are assigned to data, and the switch provides the data to the subsequent processing destination based on the processing destination identifiers, so that the data processors return the data to the switch after performing predetermined processing to the data received from the switch.